

**AMENDMENTS TO THE SPECIFICATION:**

**Please add the following new paragraphs at page 7, line 23:**

Fig. 26 is a circuit diagram showing a differential amplifier circuit according to an embodiment of the invention;

Fig. 27 is a circuit diagram showing a differential amplifier circuit according to an embodiment of the invention;

Fig. 28 is a circuit diagram showing a differential amplifier circuit according to an embodiment of the invention;

Fig. 29 is a circuit diagram showing a differential amplifier circuit according to an embodiment of the invention;

Fig. 30 is a circuit diagram showing a differential amplifier circuit according to an embodiment of the invention;

Fig. 31 is a circuit diagram showing a differential amplifier circuit according to an embodiment of the invention;

Fig. 32 is a circuit diagram showing a differential amplifier circuit according to an embodiment of the invention;

**Please add the following new paragraphs at page 26, line 1:**

Fig. 26 is a circuit diagram showing a differential amplifier circuit according to an embodiment of the invention. The differential amplifier circuit of Fig. 26 is similar to the

differential amplifier circuit of Fig. 8, but includes the transistor 30' of Fig. 11A, which is supplied with the current control signal CCS.

Fig. 27 is a circuit diagram showing a differential amplifier circuit according to an embodiment of the invention. The differential amplifier circuit of Fig. 27 is similar to the differential amplifier circuit of Fig. 9, but includes the transistor 30' of Fig. 11A, which is supplied with the current control signal CCS.

Fig. 28 is a circuit diagram showing a differential amplifier circuit according to an embodiment of the invention. The differential amplifier circuit of Fig. 28 is similar to the differential amplifier circuit of Fig. 13, but includes the transistor 30' of Fig. 11A, which is supplied with the current control signal CCS.

Fig. 29 is a circuit diagram showing a differential amplifier circuit according to an embodiment of the invention. The differential amplifier circuit of Fig. 29 is similar to the differential amplifier circuit of Fig. 14, but includes the transistor 30' of Fig. 11A, which is supplied with the current control signal CCS.

Fig. 30 is a circuit diagram showing a differential amplifier circuit according to an embodiment of the invention. The differential amplifier circuit of Fig. 30 is similar to the differential amplifier circuit of Fig. 15, but includes the transistor 30' of Fig. 11A, which is supplied with the current control signal CCS.

Fig. 31 is a circuit diagram showing a differential amplifier circuit according to an embodiment of the invention. The differential amplifier circuit of Fig. 31 is similar to the differential amplifier circuit of Fig. 16, but includes the transistor 30' of Fig. 11A, which is supplied with the current control signal CCS.

Fig. 32 is a circuit diagram showing a differential amplifier circuit according to an embodiment of the invention. The differential amplifier circuit of Fig. 32 is similar to the differential amplifier circuit of Fig. 17, but includes the transistor 30' of Fig. 11A, which is supplied with the current control signal CCS.